

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Osamu CHIKAGAWA et al.	International Application No.: PCT/JP2005/019286
Application No.: Currently unknown	
Filing or 371(c) Date: Concurrently herewith	International Filing Date: October 20, 2005
Title: MULTILAYER SUBSTRATE WITH BUILT-IN CHIP-TYPE ELECTRONIC COMPONENT AND METHOD FOR MANUFACTURING THE SAME	

EXPRESS REQUEST TO BEGIN NATIONAL EXAMINATION PROCEDURES

Mail Stop PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This is an express request to begin national examination procedures under 35 U.S.C. § 371(f). As required by 35 U.S.C. § 371(c), this submission includes a copy of the International Application as filed, unless a copy of the International Application has already been communicated by the International Bureau; an English language translation of the International Application as filed; an oath or declaration of the inventor(s); and the national fee.

Respectfully submitted,

Dated: August 11, 2006

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